

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: JEFFREY W. SCOTT ET AL.

Filed:

HEREWITH

For:

ISOLATION SYSTEM WITH DIGITAL COMMUNICATION ACROSS

A CAPACITIVE BARRIER

Serial No.:

10/601,411

Art Unit:

2634

Examiner: Atty Dkt:

UNKNOWN SILA:002C4

Date

addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date below:

Pursuant to 37 C.F.R. 1.8, I certify that this correspondence is being deposited with the U.S. Postal Service in an envelope

Commissioner For Patents

P. O. Box 1450

Alexandria, VA 22313

Dear Sir:

SUPPLEMENTAL PRELIMINARY AMENDMENT

Please amend the application as follows.

In the specification:

On page 7, please replace the paragraph at lines 23-24 with the following amended paragraph:

Figures 6A and 6B are is a schematic diagrams of the active diode bridge circuit that is used as a power supply in preferred embodiments of the present invention.